

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Naoki YAMAMOTO

Serial No.: Not yet assigned  
Divisional Application of Serial No. 09/639,306, Issued as  
Patent No. 6,645,799

Filed: Even date herewith

For: **METHOD OF MANUFACTURING A DUAL GATE  
SEMICONDUCTOR DEVICE WITH A POLY-METAL  
ELECTRODE (As Amended)**

Group: 2813 (expected)

Examiner: Jack S. J. Chen (expected)

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

February 27, 2004

Sir:

Prior to initial examination on the merits, please amend the above-identified  
application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Remarks** begin on page 5 of this paper.